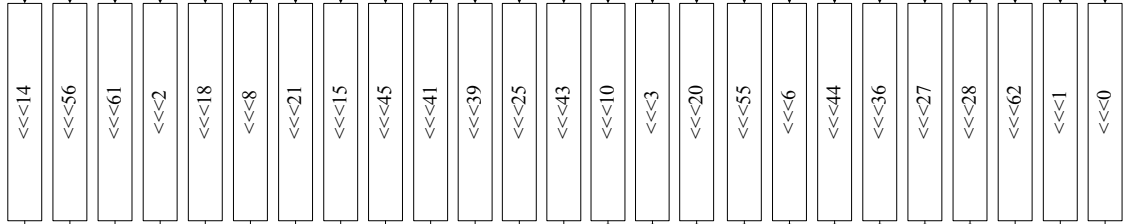


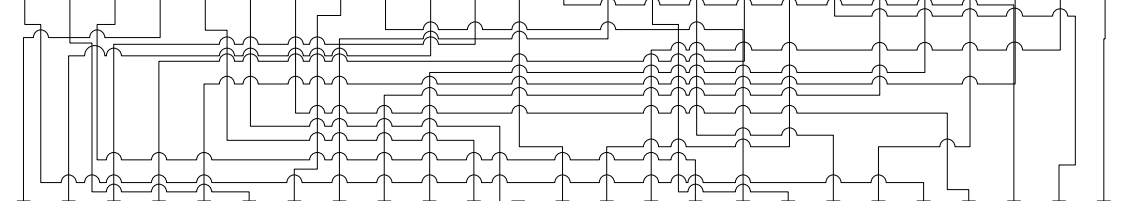
*theta*

Note: All buses size are 64 bits unless specified otherwise.  
Numbers are location of register.

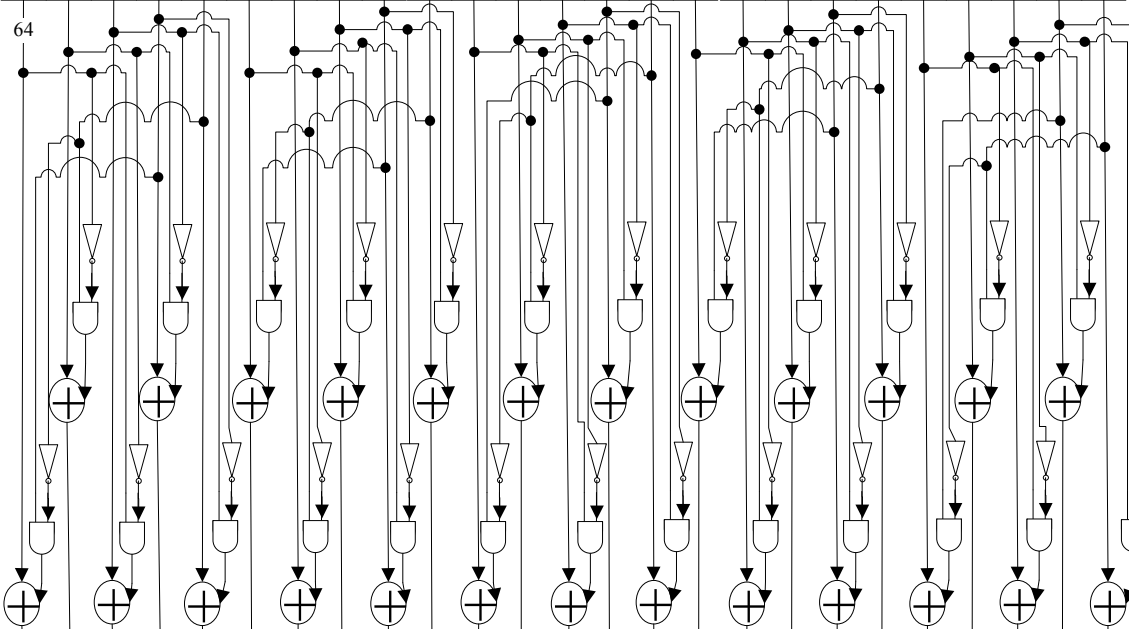
1600



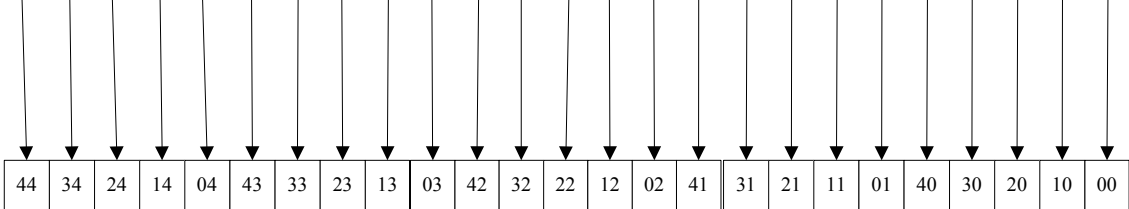
RHO



PI



CHI



IOTA



1600

*Y*